Curriculum vitae
Nicola Vincenti
(updated to 26th October 2022)

PERSONAL DATA

Name: Nicola   Telephone: +39 050/9662935
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Date of birth: 10th May 1975   E-mail: nicola@vinxent.it
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City: Pisa

ACADEMIC STUDIES

Master in microelectronic design, Università degli Studi di Padova, May 2007, Pass with merit.
Degree in electronic engineering, Università degli Studi di Pisa, December 2002, 108 out of 110.
Subscribed to corporate engeineers register of Pisa, year 2003
Diploma of teoria e solfeggio, La Spezia, 2001.

SPOKEN LANGUAGES

Trinity College SEW (Spoken English at work) Level3 (B2), December 2011.
Cambridge FCE (First Certificate in English), June 2001.
Italian native speaker - High school certificate. Liceo scientifico.

JOB EXPERIENCES

November 2021 -
Lab assistant in Scuola Superiore S. Anna in Pisa. Italy
Validation of OWC (Optical Wireless Communication) printed circuit boards in academic/space context.
Mission to Thales-Alenia Space in Rome for validation of a 1553 communication protocol and few
transceivers pcb for data stream in optical wireless link. Specific used components are (LEDs, LASERs,
PHOTODIODEs, OPAs).

September 2021 - October 2021
Employed in Feran srl (industrial automation) - Empoli - Italy
PLC programming on TIA (Totally Integrated Automations - Siemens). Programming S7 with profinet
on a plant where air flows through industrial pipes to a central nebulizer and centrifugal system for
cleaning and reconditioning of polluted air from chemical processing. Pipes flow driven by
electro-hydraulic pumps with electro-mechanical valves. Program control includes the actuation of open
and close commands on dosing-mixing device.

October 2020 - June 2021
Teacher at high school - Liceo Scienze Umane Eugenio Montale Pontedera (PI).
Subject: Maths and Physics

March 2019 - September 2020
Technical Assistant - Università degli studi di Pisa, dep. Computer Engineering
Assistant in developing hardware and software for an interface to control a Ion-Lithium BMS (Battery
Management System)

August 2018 - December 2018
Technical assistant at Colged - div. Eurotec, Turchetto Montecarlo (LU) - Italy
Technical assistant on product development and marketing

July 2016 - April 2018
Test Engineer at Microtest srl, Altopascio (LU) - Italy
DC-DC linear converter maintenance of a dual site front end test program. Number of sites in parallel : 2, Marketing product code: Infineon TLE8264
DC-DC buck converter implementation of an eight site test program (Power Mos external to the IC). Number of sites in parallel: 8, Marketing product code: not available
DC half bridge 3 phase low power electrical motor driver/controller test program adjust from 2 sites to 4 sites. Marketing product code: Infineon TLE7188
Software utility design and assistance for wafer map management and control

December 2014 - June 2016
Teacher at high school - Lucca
Electronic technologies, Computer science, Mathematics and Physics

August 2014 - October 2014
Test Engineer at Hays Infineon, Graz - Austria
Porting test program from single site testing to eight sites parallel testing. Market segment: automotive, camera image sensors (time of flight)

March 2012 - December 2013
Application Engineer at ST Microelectronics, Agrate Brianza (MI)
Design and development of application board for validating technology of a product
in the market of DC-DC converters for low power application. Marketing code: STM PM7744

February 2011 - May 2012
Test Engineer at Intel, Swindon - United Kingdom
Porting test program from Credence automated test system to Teradyne Ultraflex automated test system. Both front end wafer testing and back end final test.
Market segment: Satellite receiver. Marketing internal product code: Intel - Puma5

December 2006 - November 2009
Test Engineer in Sensor Dynamics AG, Navacchio - Italia / Graz - Austria
Functional and parametric verification of integrated circuits, test bench analysis, specification analysis, script development and statistical analysis.
Market segment: microcontrollers for accelerometers and gyroscopes and microcontrollers for current sensors (hall effect). Product code: SD710, SD410.

June 2006 - December 2006
Stage in Istituto Nazionale di Fisica Nucleare (INFN), Ferrara - Italia
Designing software architecture for satellite telecommunication systems (WRFF - Window Radio First Fit).

May 2003 - August 2005
Employed in Microtest srl, Altopascio, LU
Consultancy on design and development of test boards for wafer testing and final test. Development of software program in steps. Specification analisys. Market segment: automotive microcontrollers ST10 family.

Techincal skills
Hardware description languages: VHDL, FPGA: XILINX, ALTERA.
Scripting Python e PHP.
PCB design.

Layout of components on PCB.

Integrated circuits verification and validation.

Library components available in design: relays, inductors, capacitors, resistors, diodes, bipolar transistors, field effect transistors, operational amplifiers, combinatory and sequential logics, microcontrollers.

Frequency range of interest: 20 Hz - 100 MHz.

ATE instrumentation

Teradyne J750: DPS, CTO, MSO, DIgital Channels, PPMU, BPMU.

Teradyne Microflex: DC30, DC90, POOL, Digital Channels, PPMU, DC Diff Meter.

Electronic lab instrumentation

power supply, oscilloscope, multimeter, wafer generator.

Pisa, 26th October 2022