

Curriculum vitæ
Nicola Vincenti
(updated to 30th November 2018)

PERSONAL DATA

Name: Nicola	Home number:+39 050 / 9662935
Surname: Vincenti	Mobile: +39 340 / 8906805
Date of birth: 10th May 1975	E-mail: nicola@vinxent.it
Address: Via Lungarno Gambacorti n. 53	Driving License: Type B
CAP: 56125	Website: www.vinxent.it
City: Pisa	

ACADEMIC STUDIES

Master in Microelectronic design, University of Padova, May 2007, Pass with merit.
Degree in electronic engineering, University of Pisa, December 2002, grade 108/110.
Subscribed to corporate engineers register, year 2003.
Diploma of teoria e solfeggio, La Spezia, 2001.

SPOKEN LANGUAGES

Trinity College SEW (Spoken English for Work) Level 3 (B2), December 2011.
Cambridge FCE (First Certificate in English), June 2001.

JOB EXPERIENCES

August 2018 - December 2018

Technical assistant at Colged - div. Eurotec, Turchetto - Montecarlo (LU) - Italy
Technical assistance on product development and marketing.

July 2016 - April 2018

Test Engineer at Microtest srl, Altopascio (LU) - Italy

DC-DC linear converter maintenance of a dual site front end test program.
DC-DC buck converter implementation of an eight site test program.
DC half bridge 3 phase low power electrical motor driver/controller test program adjust from 2 site to 4 site.
Software utility design and assistance for wafer map management and control.

December 2014 - June 2016

Teacher at High School Lucca - Italy

Electronic technologies, Computer science, Mathematics and Physics.

August 2014 - October 2014

Test Engineer at Hays Infineon, Graz - Austria

Porting test program from single site to multi site testing.

March 2012 - December 2013**Application Engineer at ST Microelectronics, Agrate Brianza (MI)**

Design and development of hardware and software for printed circuit board on specific commitment from customers.

Market segment: DC-DC voltage converter for low power consumer applications.

February 2011 - May 2012**Test Engineer at Intel, Swindon - United Kingdom**

Development of test program both for wafer sort and package test.

Market segment: set top box satellite telecommunication systems.

December 2006 - November 2009**Test Engineer at Sensor Dynamics AG, Navacchio - Italy / Graz - Austria**

Functional and parametric verification of integrated circuits, test bench analysis, test specification analysis, test program development. Scripting. Statistical analysis.

Market segment: accelerometer and gyro microcontrollers.

June 2006 - December 2006**Internship at Istituto Nazionale di Fisica Nucleare (INFN), Ferrara - Italy**

Design and development of algorithms for telecommunication.

May 2003 - August 2005

Employed in Microtest srl, Altopascio, LU

Consultant on design and development of boards and probe cards for automated test systems, Verification and validation programs in many steps. Test specification analysis. Mediator between customers and suppliers.

Market segment: Digital microcontrollers.

Achieved technical skills

Hardware design languages: VHDL, FPGA: XILINX, ALTERA.

Software design languages VB, C/C++ and Java, either for uC and PC.

Scripting Python and PHP.

Printed circuit board design.

Layout design.

Verification and validation of circuit design.

Components library: relays, inductors, capacitors, resistors, diodes, bipolar junction transistors, metal oxide semiconductor field effect transistors, operational amplifiers, combinatory and sequential logics, microcontrollers.

Frequency range of interest: 20 Hz - 100 Mhz.

ATE Instrumentation

Teradyne J750: DPS, CTO, MSO, Digital Channels, PPMU, BPMU.

Teradyne MicroFlex: DC30, DC90, POOL, Digital Channels, PPMU, DC Diff Meter.

Electronic lab instrumentation

power supply, oscilloscope, multimeter, waveform generator.

Pisa, 30th November 2018